

## WEST Search History

DATE: Saturday, July 26, 2003

Set Name Query

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result set

*DB=USPT; PLUR=YES; OP=ADJ*

L5	6173381[uref]	6	L5
L4	6173381.pn.	1	L4
L3	6138180.pn. and (driver)	1	L3
L2	6138180.pn. and (universl or usb or ieee)	0	L2
L1	6138180.pn. and (serial\$ or usb or ieee)	1	L1

END OF SEARCH HISTORY

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L4: Entry 1 of 1

File: USPT

Jan 9, 2001

DOCUMENT-IDENTIFIER: US 6173381 B1

TITLE: Memory controller including embedded data compression and  
decompression enginesUS Patent No. (1):6173381

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L5: Entry 2 of 6

File: USPT

May 13, 2003

DOCUMENT-IDENTIFIER: US 6564305 B1

TITLE: Compressing memory management in a device

US Reference Patent Number (12):

6173381

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L5: Entry 3 of 6

File: USPT

Feb 18, 2003

US-PAT-NO: 6523102

DOCUMENT-IDENTIFIER: US 6523102 B1

TITLE: PARALLEL COMPRESSION/DECOMPRESSION SYSTEM AND METHOD FOR  
IMPLEMENTATION OF IN-MEMORY COMPRESSED CACHE IMPROVING STORAGE  
DENSITY AND ACCESS SPEED FOR INDUSTRY STANDARD MEMORY SUBSYSTEMS  
AND IN-LINE MEMORY MODULES

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L5: Entry 4 of 6

File: USPT

Sep 17, 2002

US-PAT-NO: 6452602

DOCUMENT-IDENTIFIER: US 6452602 B1

TITLE: Method and apparatus for storing compressed data